

FIGURE 1

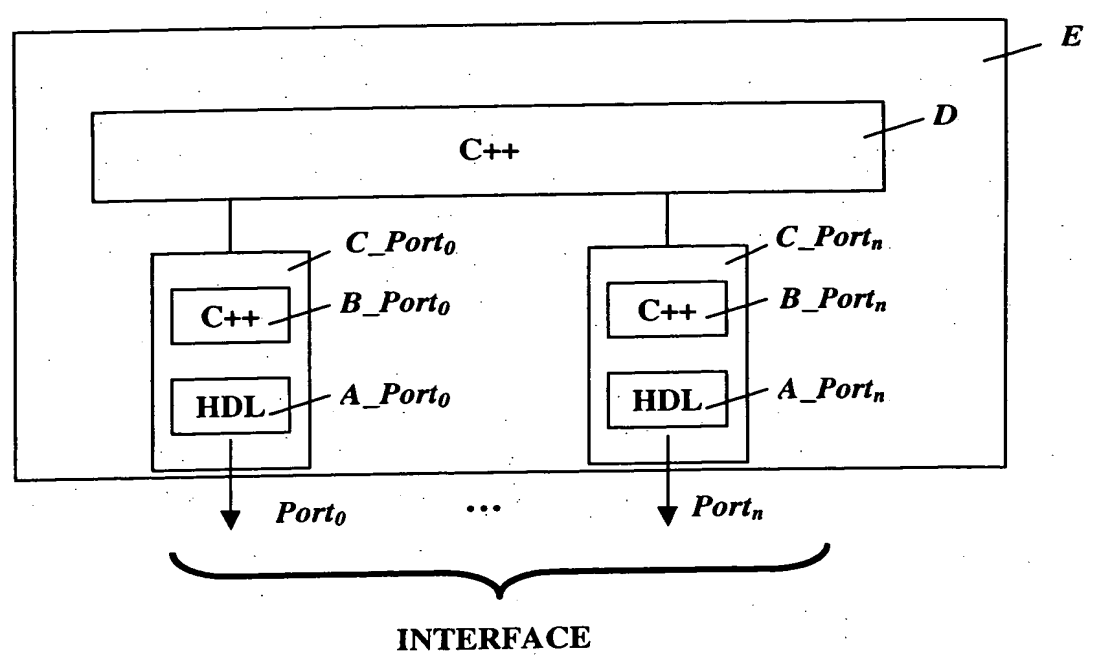


Figure 5 : Generic structure of a C++ Composite Model

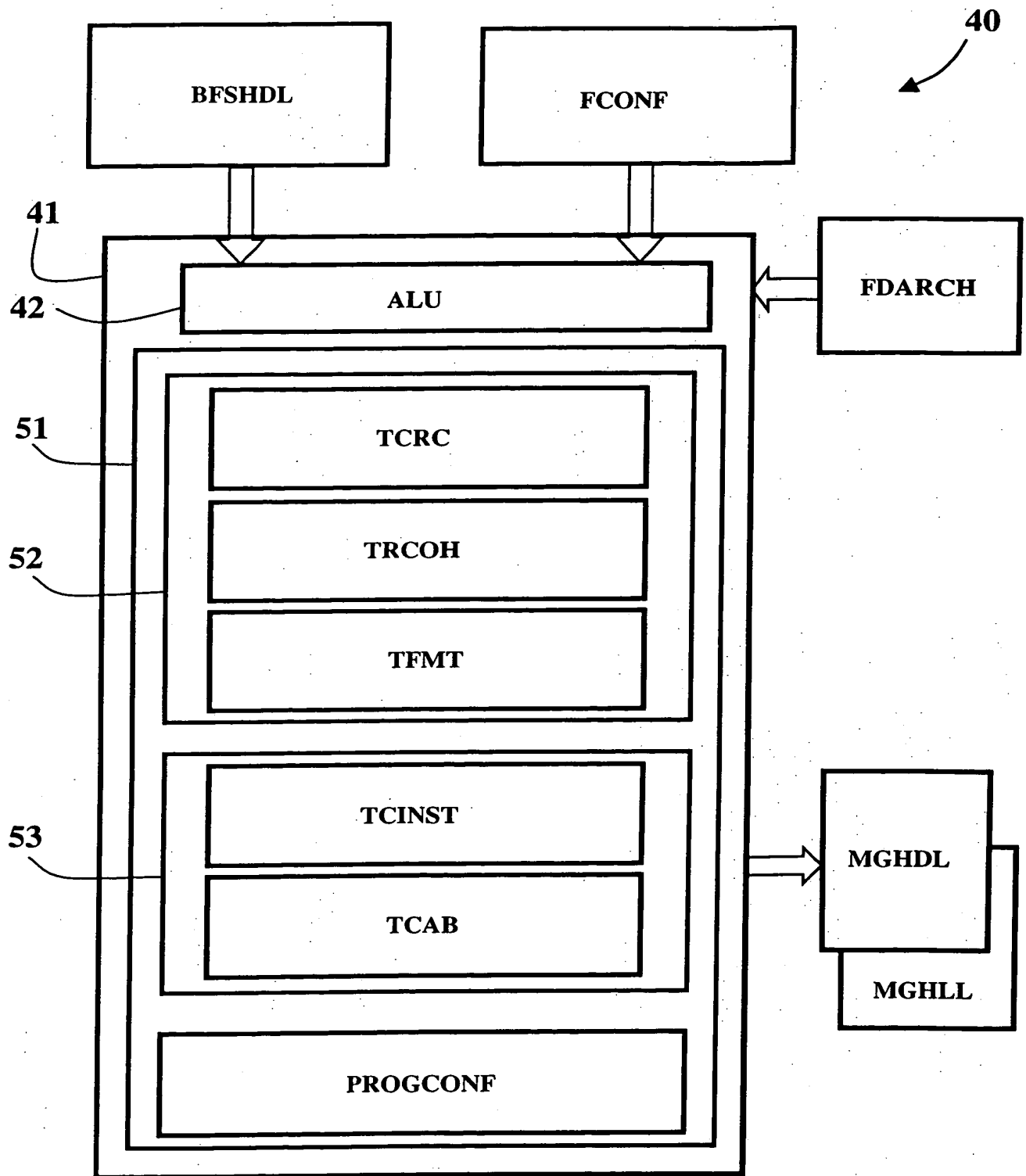


FIGURE 2A

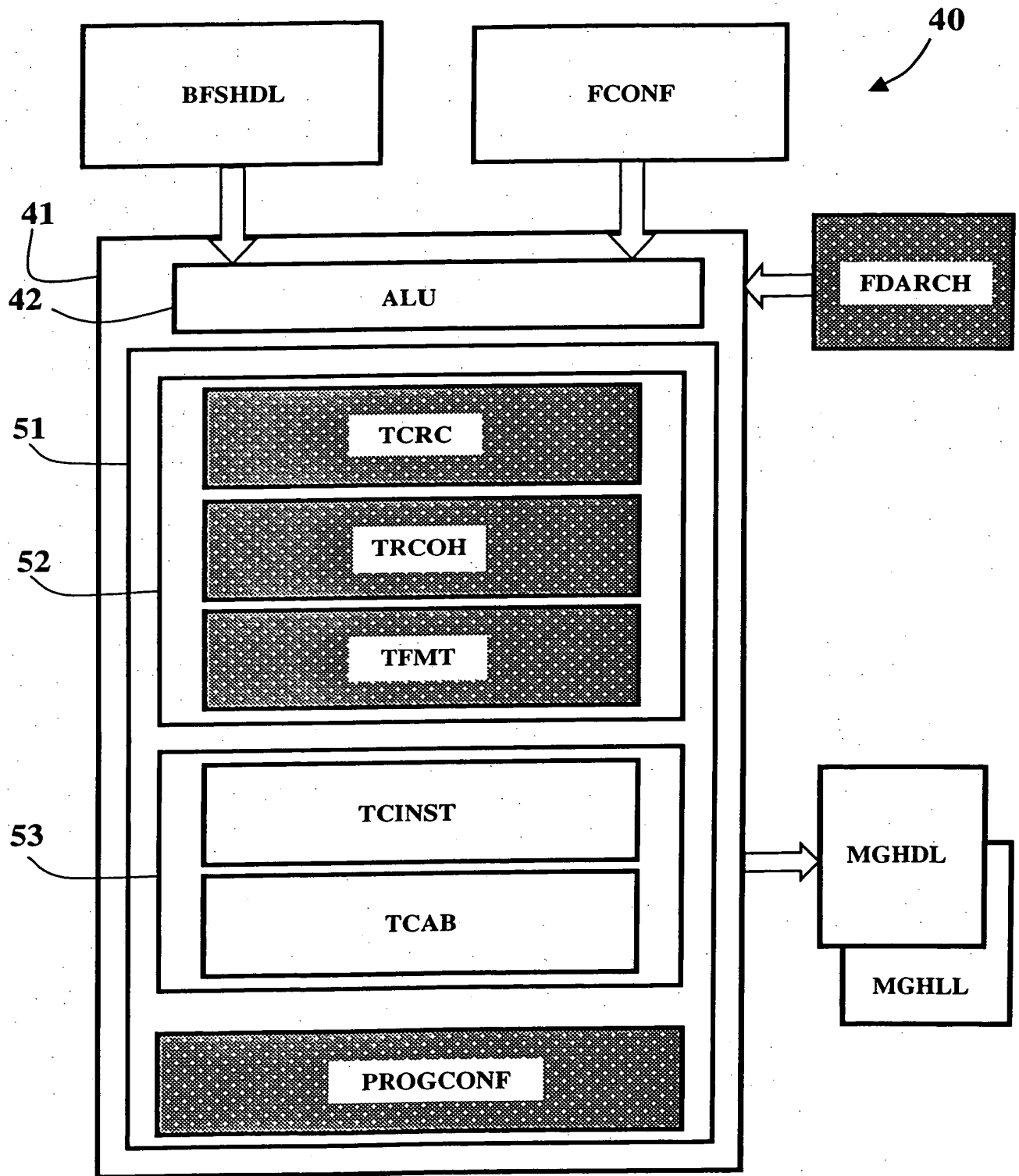


FIGURE 2B

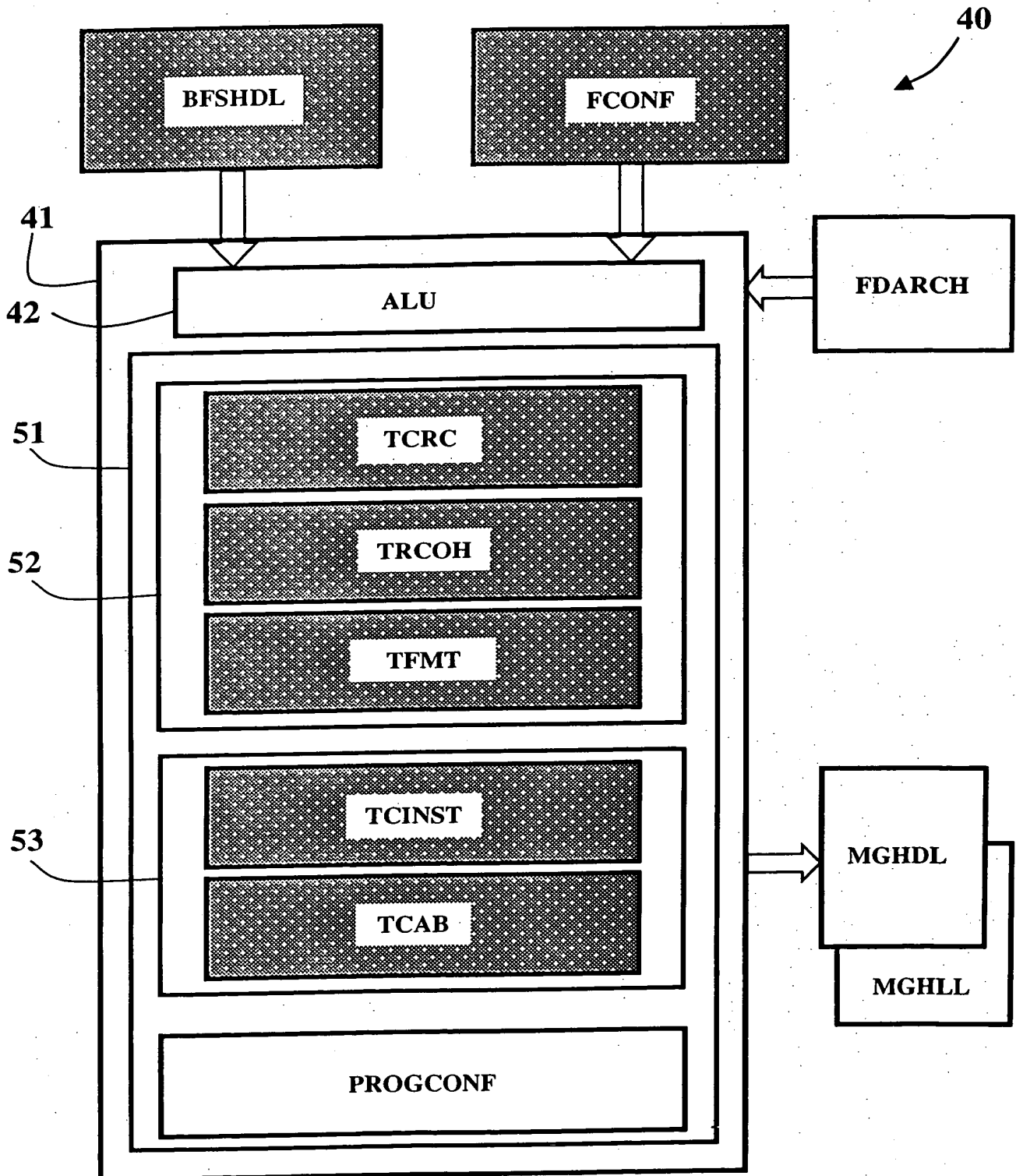


FIGURE 2C

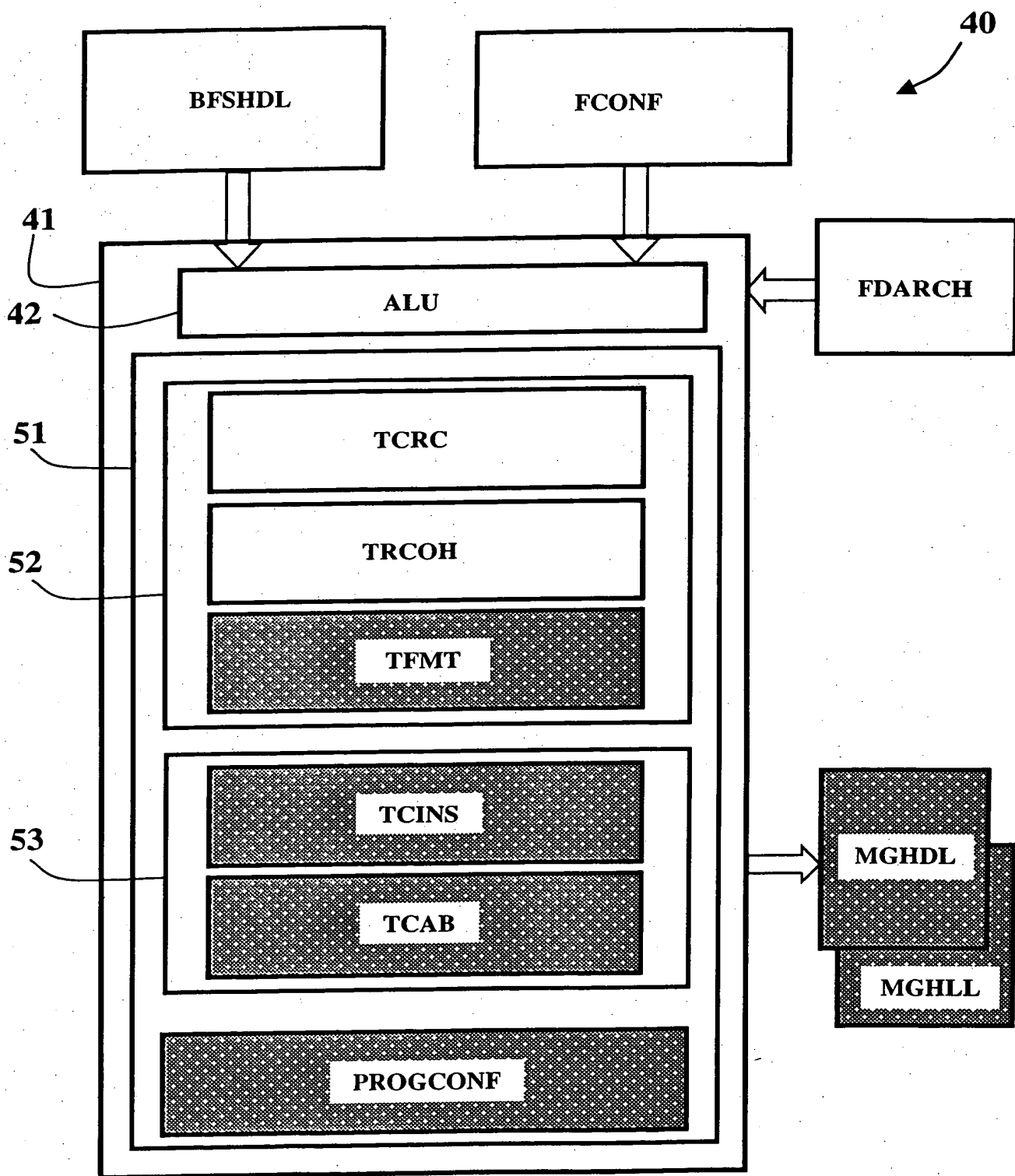
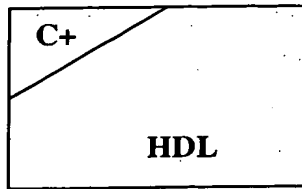
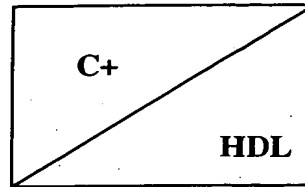


FIGURE 2D



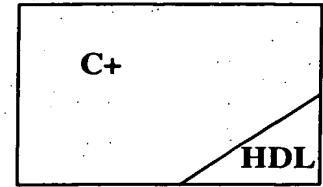
HDL Modeling

Figure 3A



General case

Figure 3B



C++ Modeling

Figure 3C

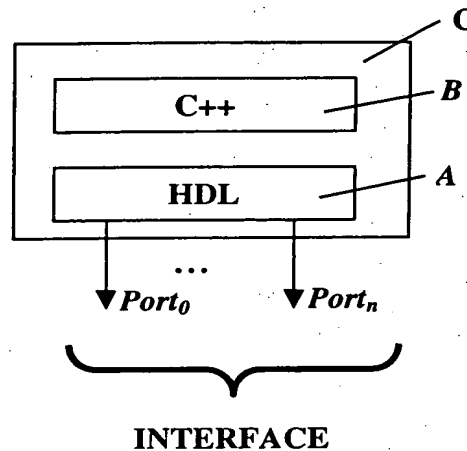


Figure 4 : Generic Structure of an Elementary Model

TCRC	%InstNameModuleMap
TCRC	%SysConnectMap
TCRC	%SysSpecMap
TCRC	%SysWrapMap
TCRC	%SysPinConst
TCRC	%Activity_TypeMap
TCRC	%PortProbeMap
TRCOH	%HwfConnectivityMap
TRCOH	%HwifAlternateMap
TFMT	%moduleToCppClassMap
TFMT	%classCppProtoMap

Figure 6

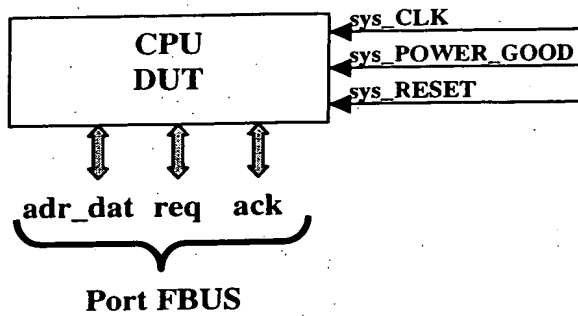


Figure 7A

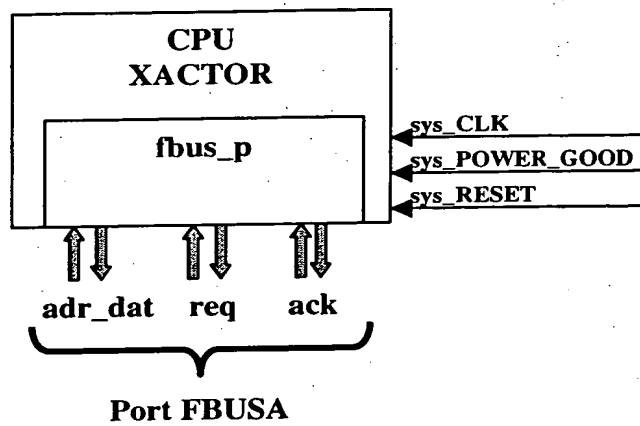


Figure 7B

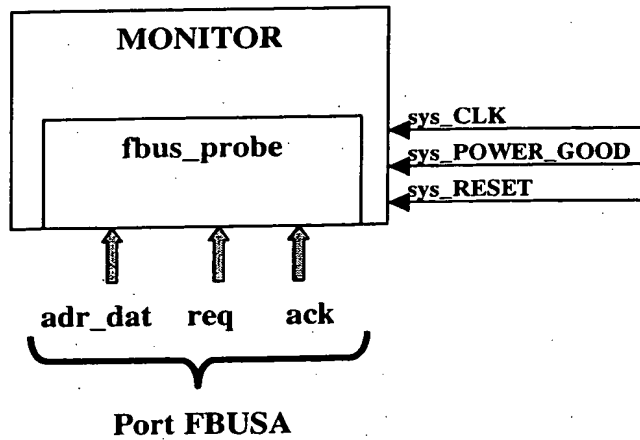


Figure 7C

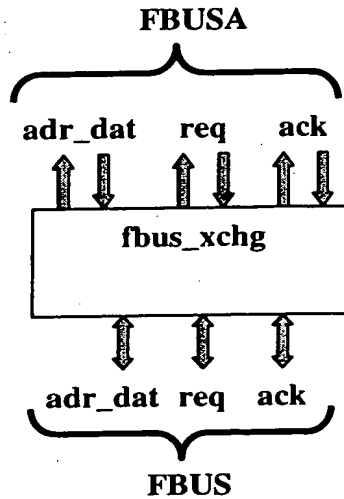


Figure 7D

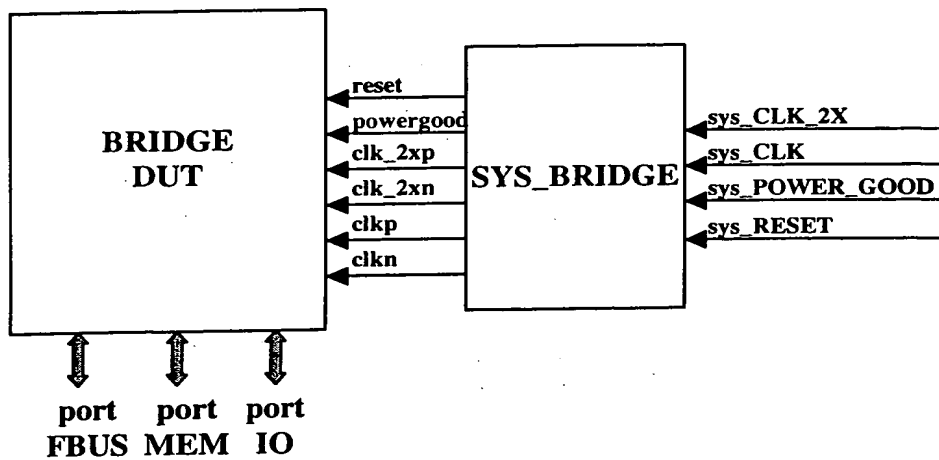


Figure 7E

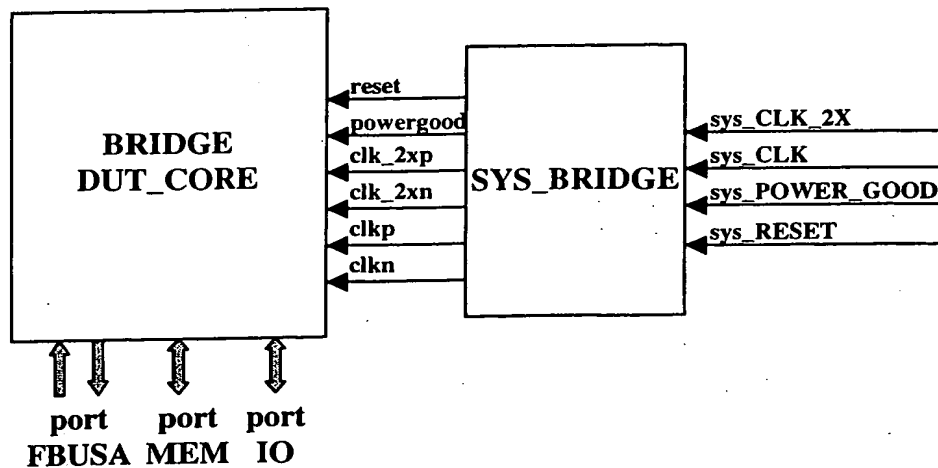


Figure 7F

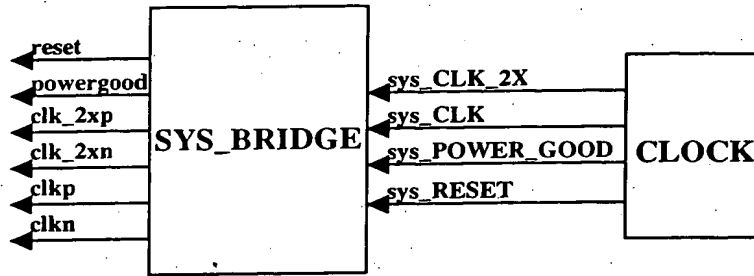


Figure 7G

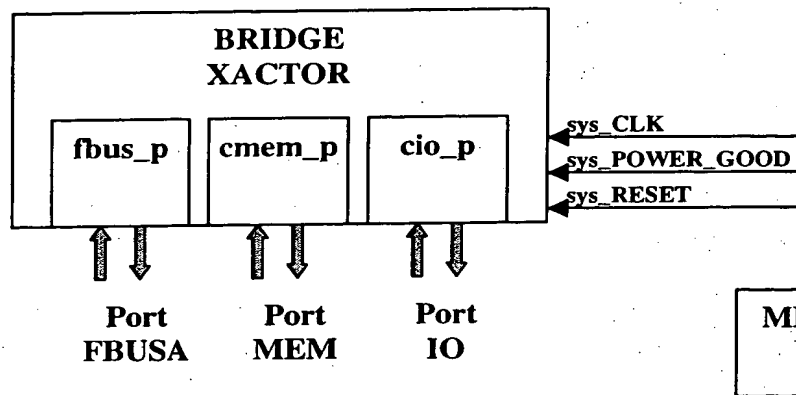


Figure 7H

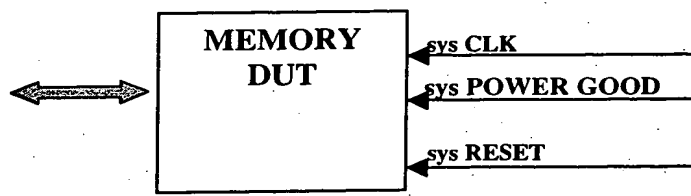


Figure 7I

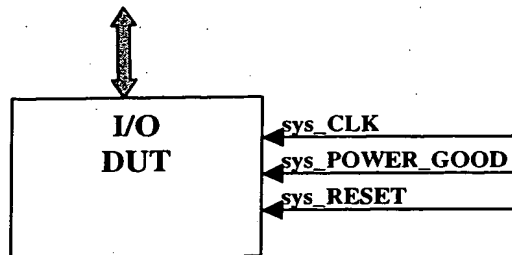


Figure 7J

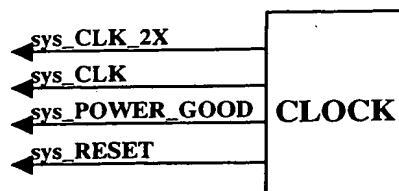


Figure 7K

Figure 8A

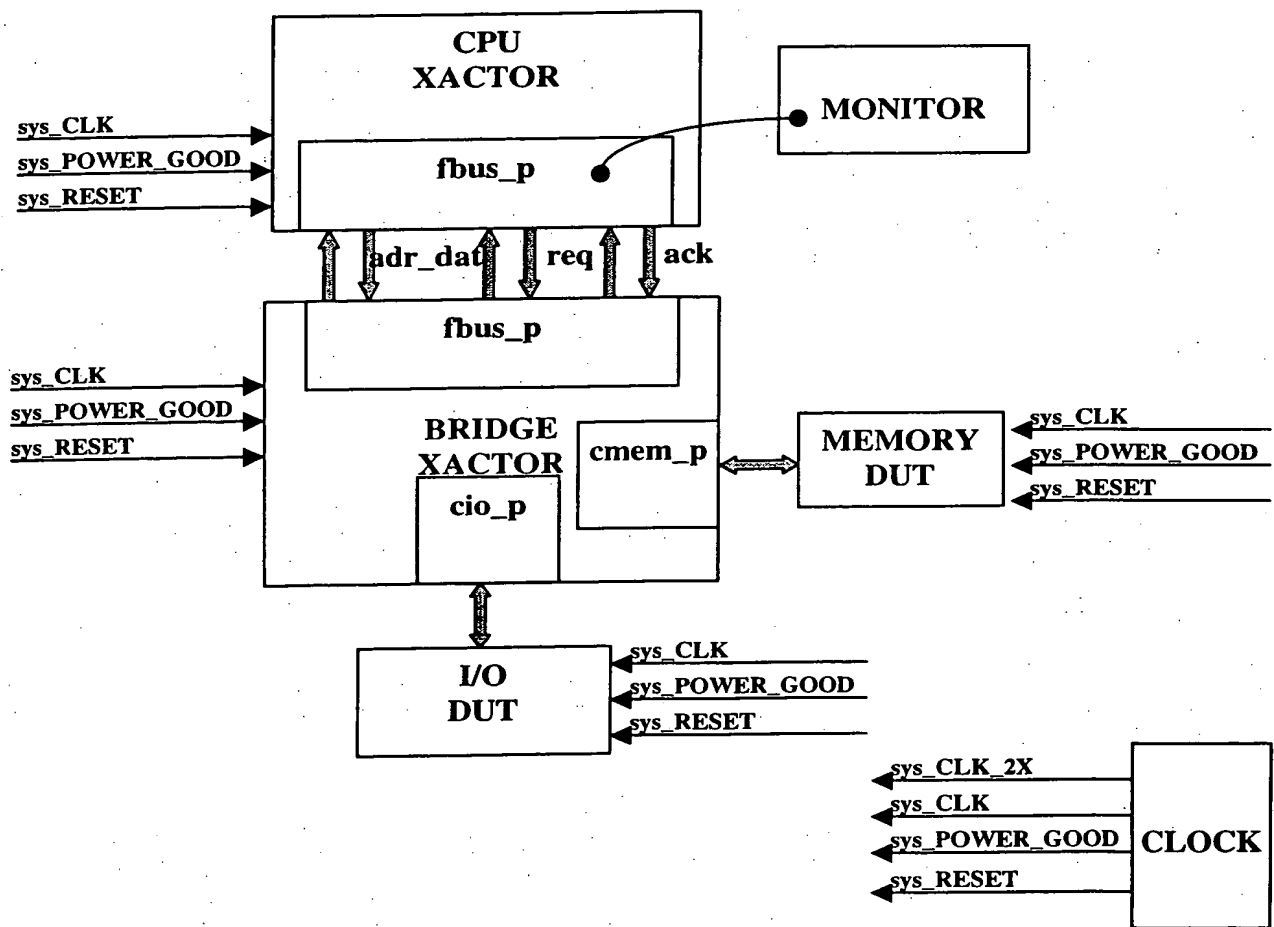
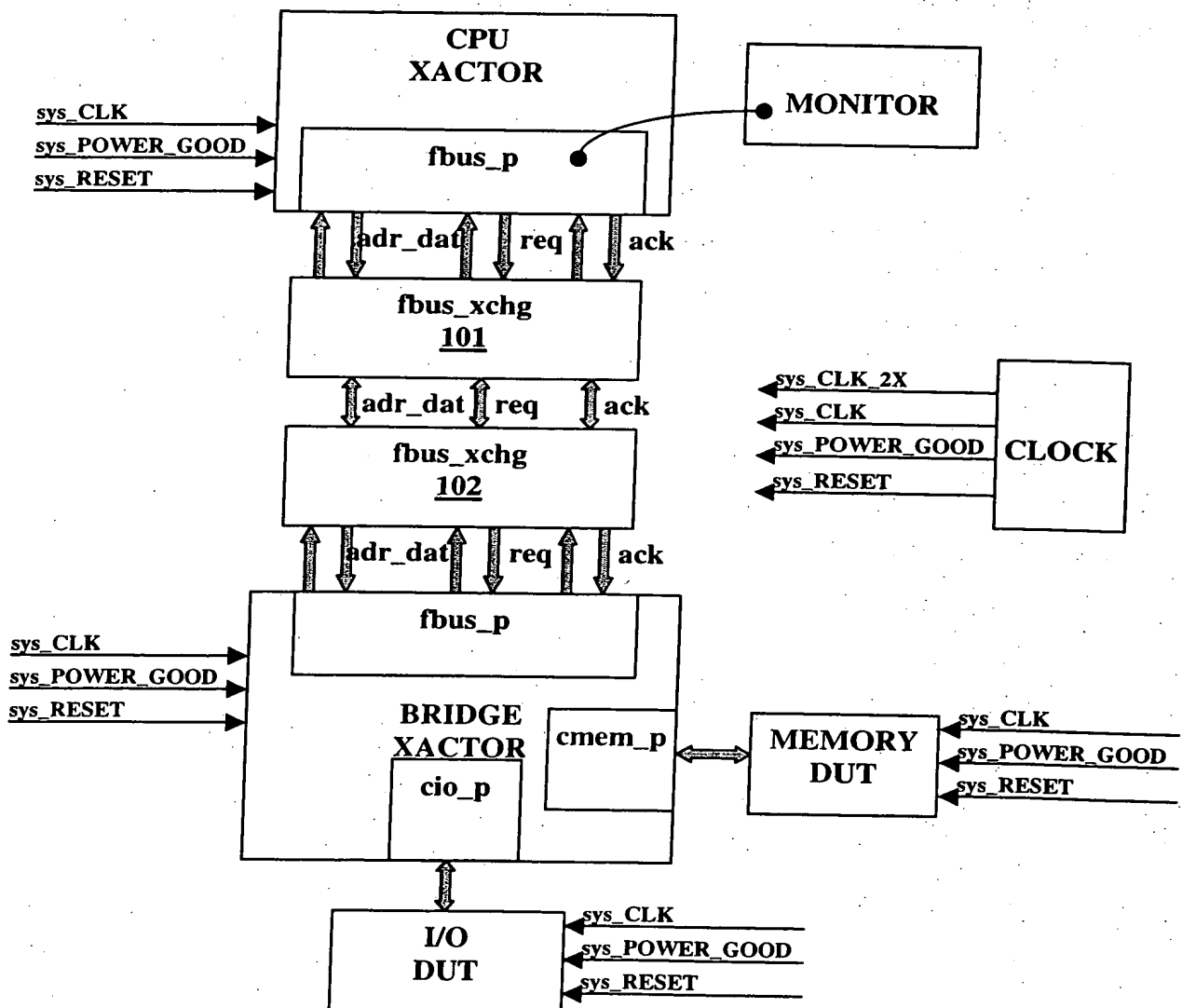


Figure 8B



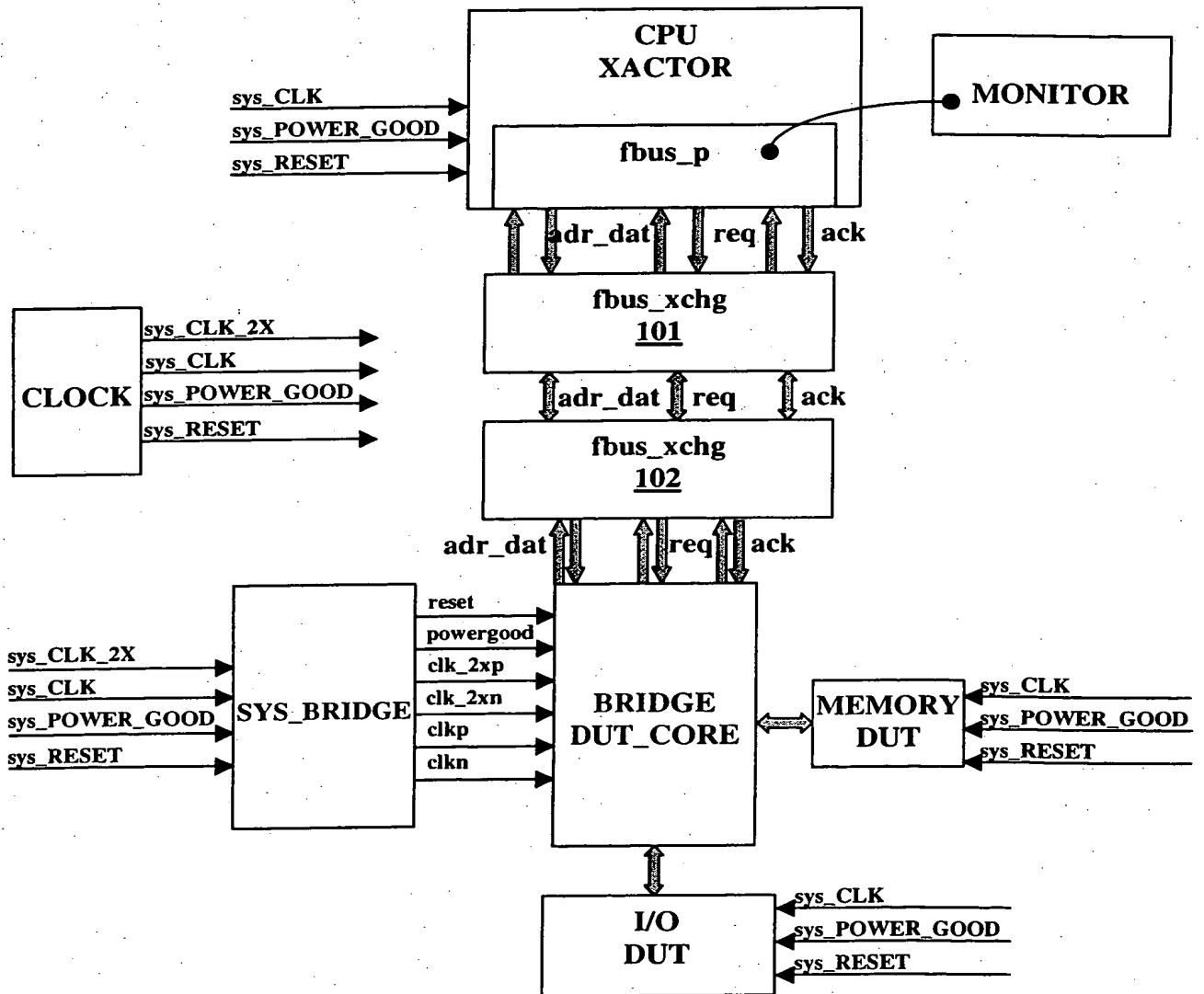


Figure 8C

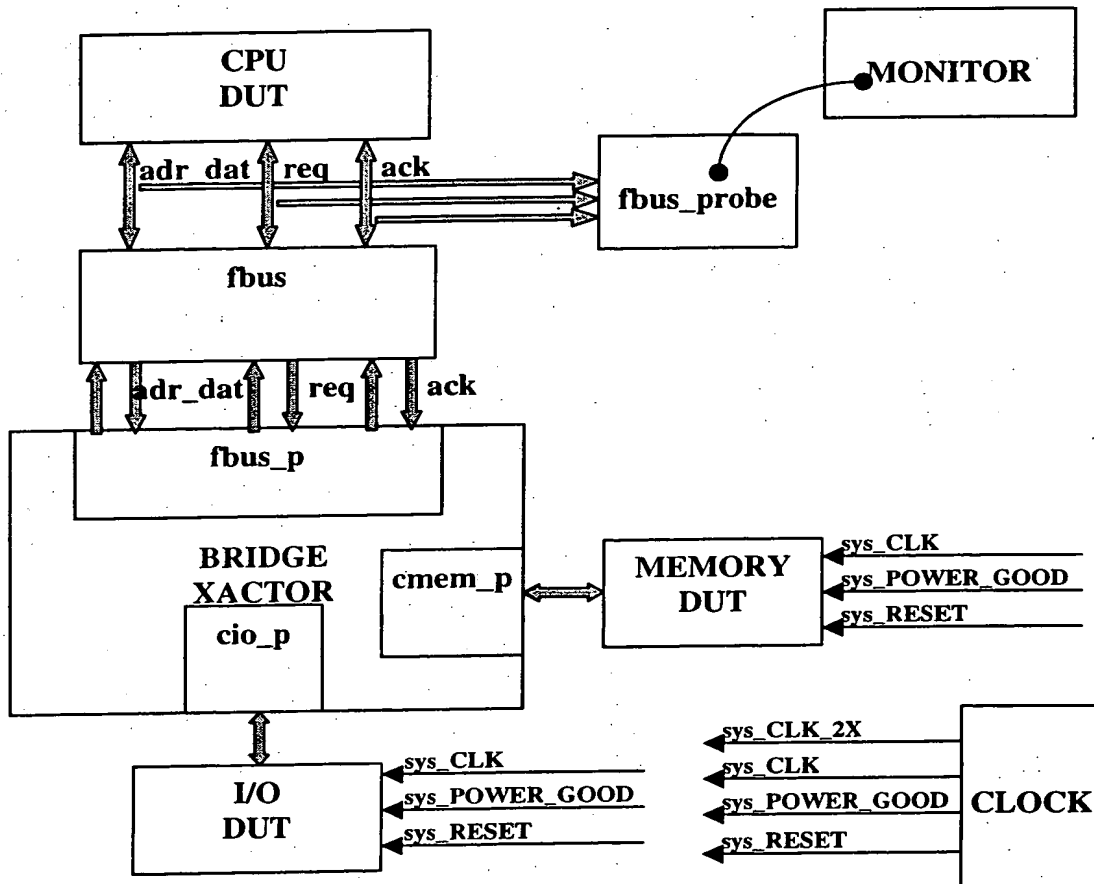


Figure 8D

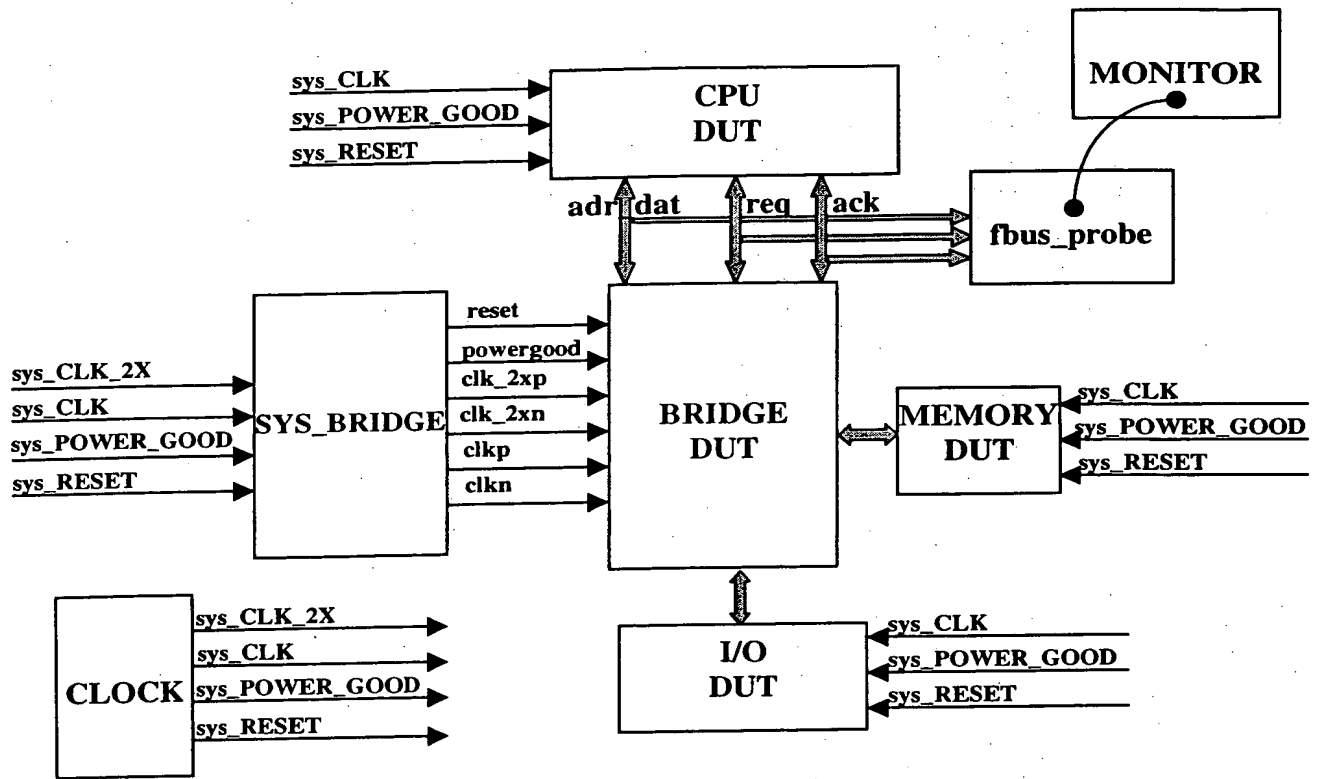


Figure 8E

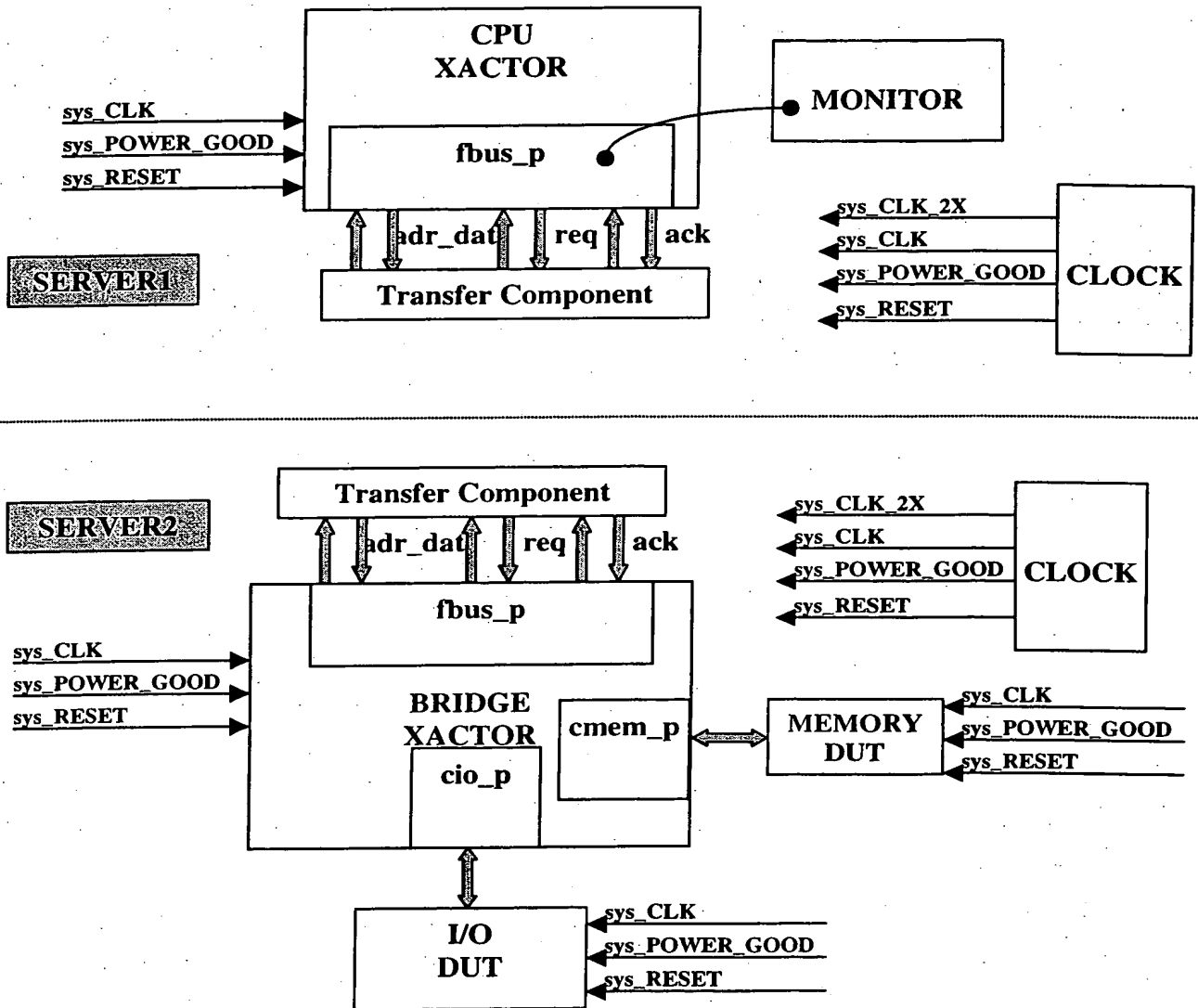


Figure 8F